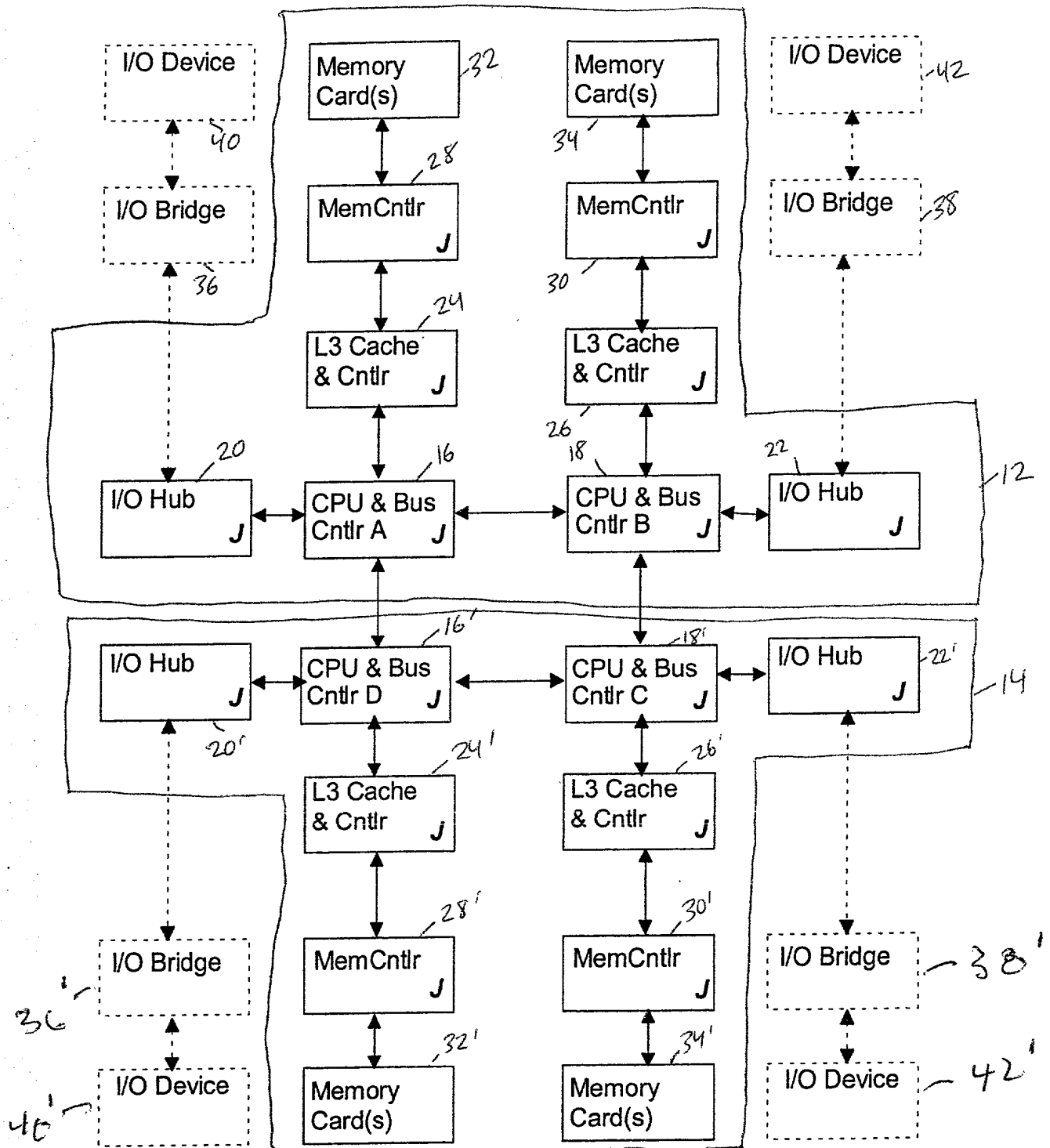


118
 AUS920070 114US1

ET150396423US



J = chip has serial jtag connection to Service Processor HW

[dotted box] dotted box indicates remote chip or device inaccessible to SP via jtag

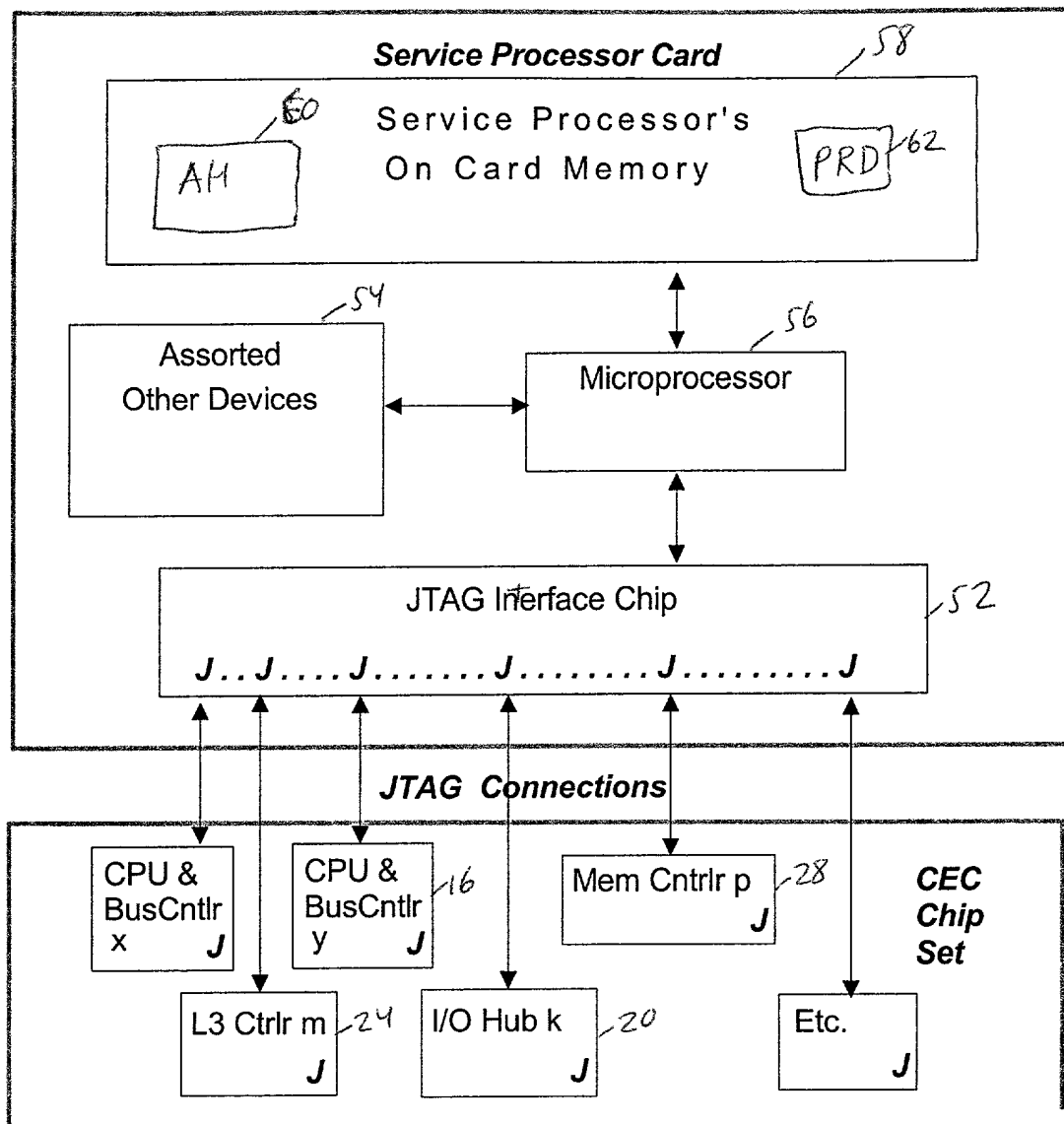
[dotted line] dotted line indicates connections to and between remote chips & devices

10

Figure 1

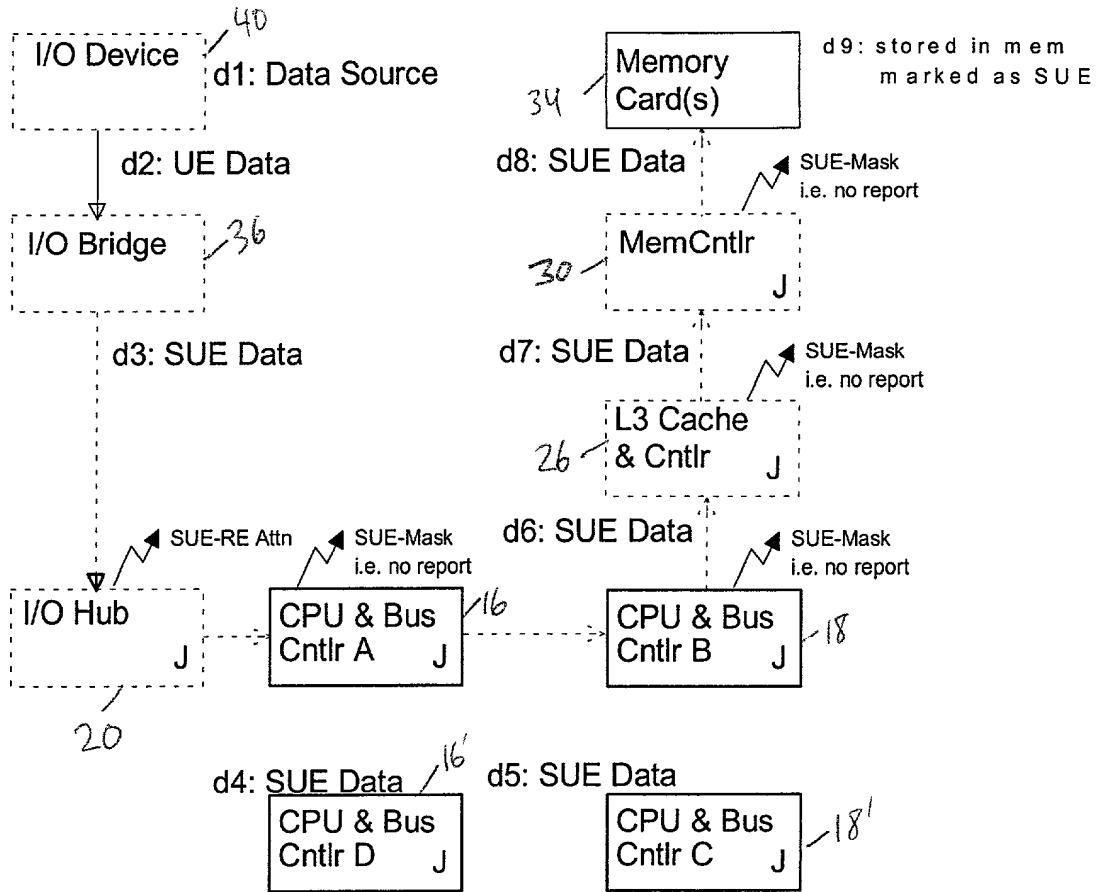
218
av5970010114US1

50



J = JTAG serial interface between Service Processor & chips in CEC

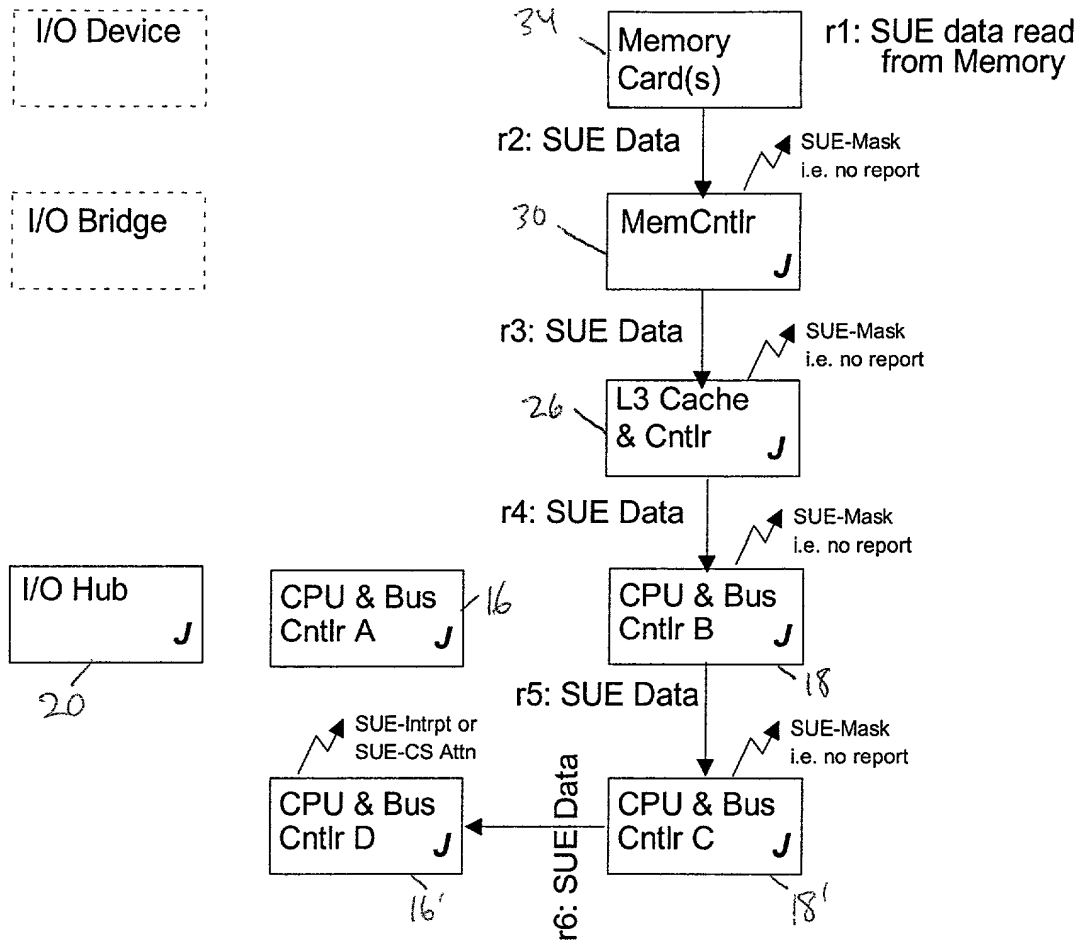
Figure 2



J = chip has serial jtag connection to Service Processor HW
 [dashed box] dotted box indicates remote chip or device inaccessible to SP via jtag
 ◄ ► dotted line indicates connections to and between remote chips & devices

Figure 3A

418
av59200,0114WS1



J = chip has serial jtag connection to Service Processor HW

[dotted box] dotted box indicates remote chip or device inaccessible to SP via jtag

◄► dotted line indicates connections to and between remote chips & devices

Figure 3B

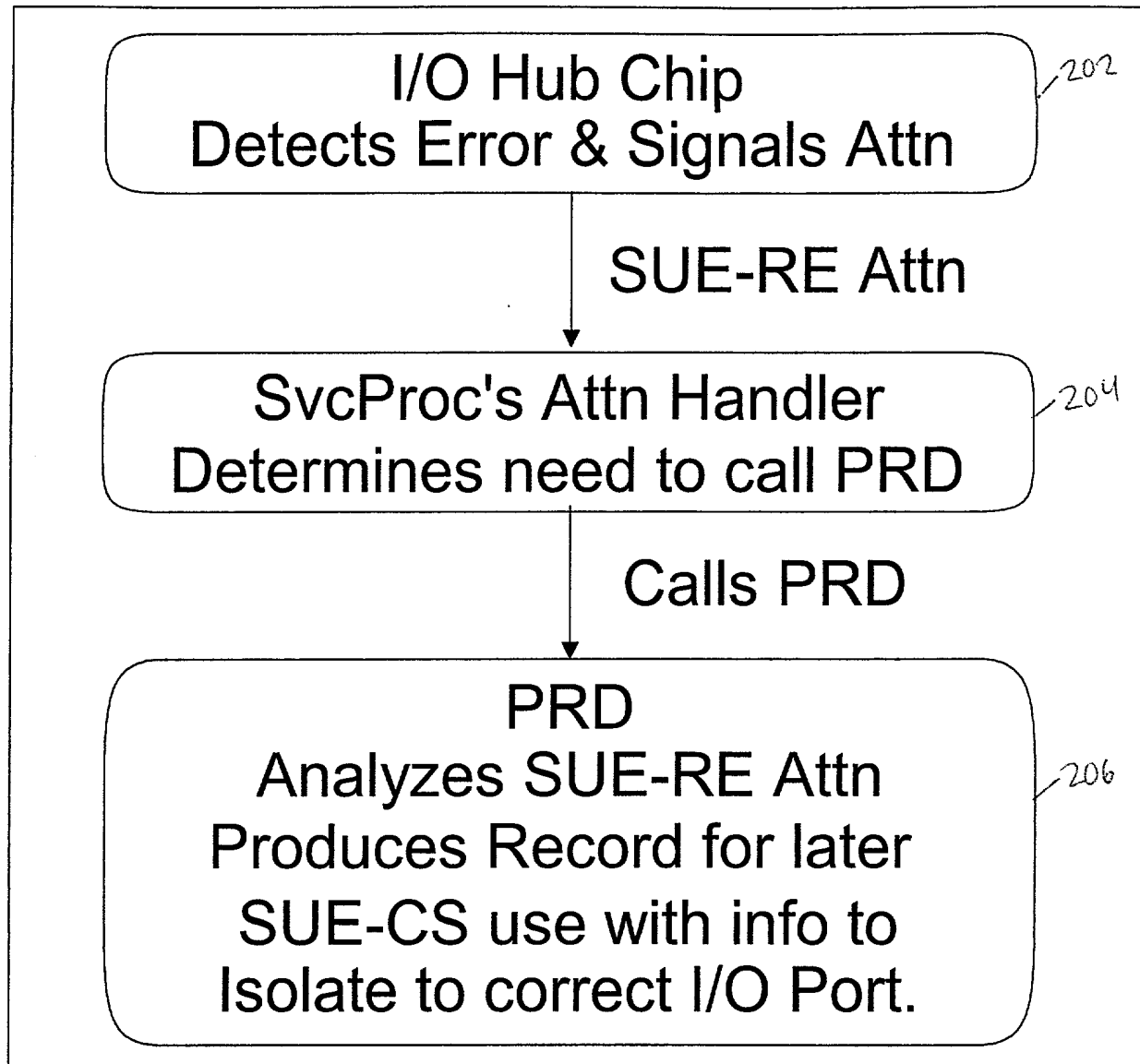


Figure 4

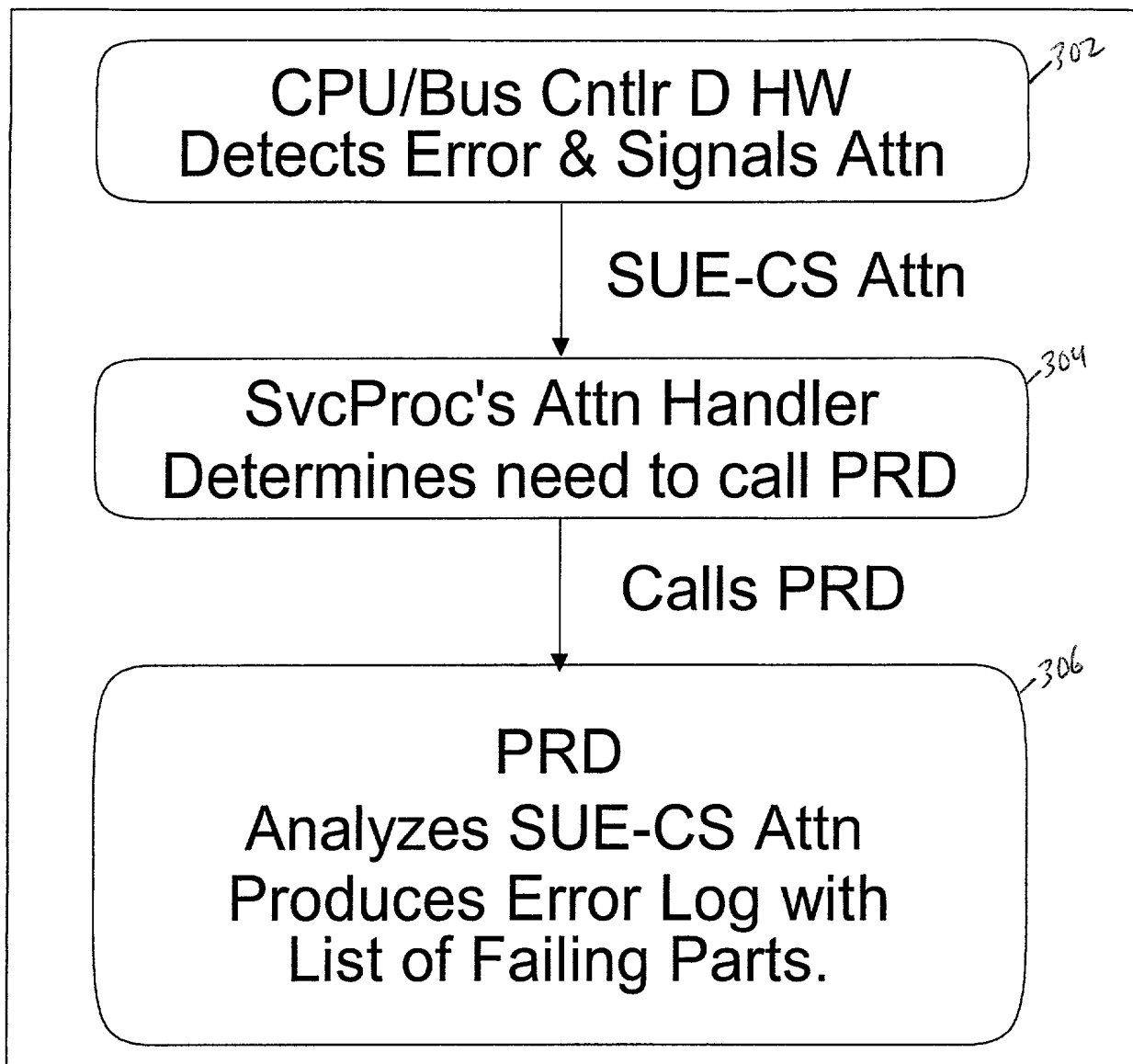
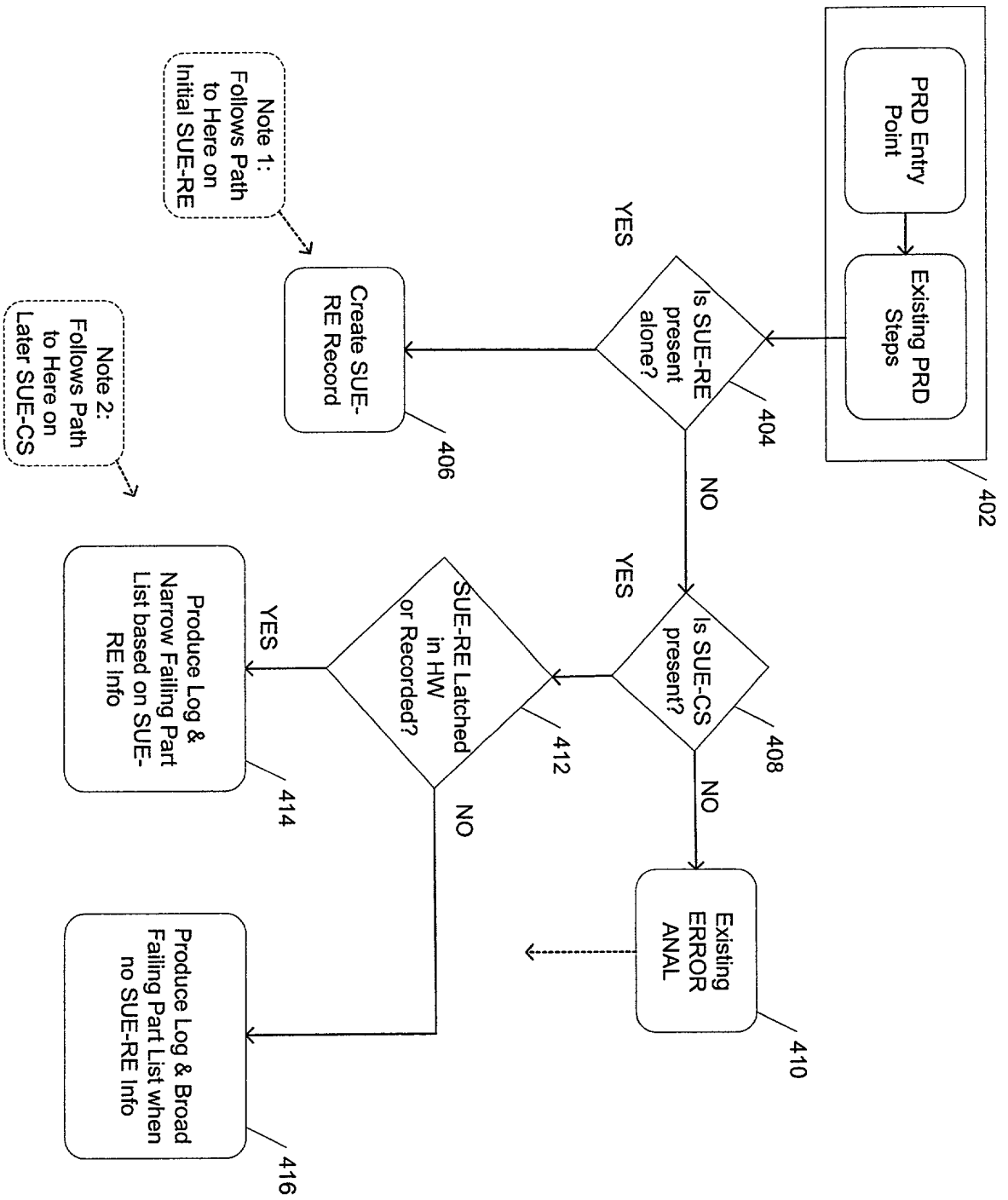


Figure 5



157411010114251
817

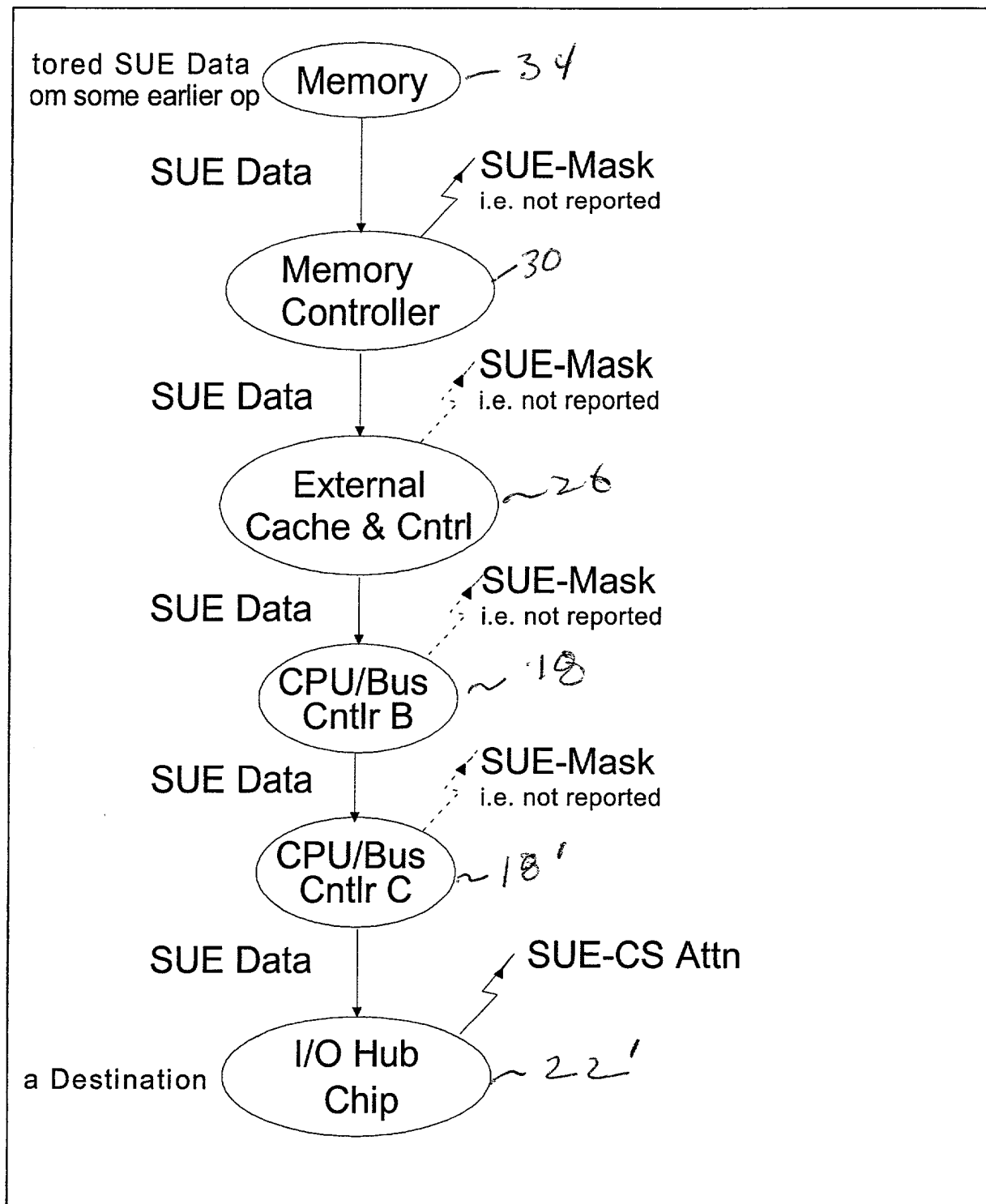


Figure 7